

MODERN POWER SYSTEMS

CONTENTS

chapter 1

INTRODUCTION.....	1
1-1. The Old and the New	1
1-2. General Objectives	2
1-3. Utilizing the Appendix	3
1-4. A New Emphasis	4
1-5. Paving the Way—Chapters 2–6	5
1-6. Short-Circuit Calculations	5
1-7. Concepts of Real and Reactive Power Flow	6
1-8. Load-Flow Studies	6
1-9. Load Forecast, Unit Commitment, and Economic Dispatch	7

chapter 2

SELECTED NETWORK CONCEPTS.....	9
2-1. Purpose	9
2-2. Network Reductions	9
2-3. Four-Terminal Networks	12
2-4. The Rake Equivalent for the Bus Impedance Matrix	15
2-5. The Rake-Mesh Equivalent Conversions	17
Problems	20

chapter 3

POWER TRANSMISSION LINES.....	24
3-1. Introduction	24
3-2. Three-Phase Transmission—Review	24
3-3. Reviewing the Concept of the Power Triangle	26
3-4. Short Transmission Lines	30
3-5. π and T Equivalents for the “Medium” Line	32
3-6. The Long Transmission Line.....	34
3-7. Long-Line Equivalents	39
3-8. Comparison of Long-Line Equivalents with the Approximate π and T Equivalents of Sec. 3-4	41
Problems.....	42

chapter 4

POWER NETWORK REPRESENTATIONS..... 44

4-1.	Introduction	44
4-2.	Synchronous Generator Representation and Review	44
4-3.	The Infinite Bus	47
4-4.	Representing Transformers	48
4-5.	Representing Lines	52
4-6.	Ohmic Representation of Components in the Network	53
4-7.	Per Unit Representation.....	55
4-8.	Expressing Mutual Impedance as a Per Unit Quantity Between Lines of Different Voltage Levels	58
4-9.	Per Unit vs. Ohmic Representation	59
4-10.	One-Line Diagrams	60
4-11.	A-C and D-C Analyzer Boards.....	61
4-12.	Representing the "Outside" System on the D-C Board	65
4-13.	Obtaining the Rake Equivalent from the Analyzer Board.....	67
4-14.	Obtaining the Mesh Equivalent from the Analyzer Board	68
4-15.	Representing Mutual Impedances on the D-C Board	69
	Problems.....	71

chapter 5

BASIC NETWORK SOLUTIONS..... 75

5-1.	Introduction	75
5-2.	The Loop-Impedance Method	76
5-3.	Elimination of Loop Currents by Matrix Partitioning	81
5-4.	The Nodal-Admittance Method	82
5-5.	Node Elimination by Matrix Partitioning	87
5-6.	Additional Network Topology	91
5-7.	The General Cut-Set Admittance Method	96
	Problems.....	99

chapter 6

**NETWORK SOLUTIONS
WITH MATRIX TRANSFORMATIONS101**

6-1.	Transformation Concepts	101
6-2.	The Branch-Impedance Transformation Method	103
6-3.	The Branch-Admittance Transformation Method	109
6-4.	Systematic Formation of the \bar{C} Matrix.....	115
6-5.	Systematic Determination of the \bar{A} Matrix	117
6-6.	General Application.....	119
	Problems.....	120

chapter 7

SYMMETRICAL FAULT STUDIES121

7-1.	Introduction	121
7-2.	Generator Transients During a Three-Phase Fault (Assuming Constant Inductance).....	121

7-3. Symmetrical Faults on Synchronous Generators—Transient Reactances Considered and Unidirectional Currents Neglected 124

7-4. Interrupting the Fault 133

7-5. Resume of a Symmetrical Fault Study 134

Problems 135

chapter 8

SYMMETRICAL COMPONENTS AND THE UNBALANCED FAULT.....137

8-1. Introduction 137

8-2. The Concept of Symmetrical Components 138

8-3. Determination of the Positive-, Negative-, and Zero-Sequence Components in Terms of the Original Phasors 139

8-4. The General Nature of Zero-Sequence Currents 144

8-5. The Line-to-Ground Fault 146

8-6. The Line-to-Line Fault 155

8-7. The Double Line-to-Ground Fault 161

8-8. Summary 165

Problems 166

chapter 9

FURTHER CONSIDERATIONS FOR UNSYMMETRICAL FAULTS AND LOADS.....169

9-1. Introduction 169

9-2. Technique of Balancing Ampere-Turns for Problem Solving 169

9-3. Sequence Impedances—General 172

9-4. Sequence Impedances for Transmission Lines 173

9-5. Sequence Impedances of Generators 175

9-6. Sequence Impedances of Transformers 180

9-7. Measurement of Sequence Voltages and Currents 183

9-8. Transformer Polarity and Phase-Shift Considerations 184

Problems 193

chapter 10

THE BUS IMPEDANCE MATRIX METHODS FOR FAULT STUDIES.....195

10-1. Introduction 195

10-2. Basic Concepts and Assumptions 196

10-3. Formation of the Short-Circuit Matrix 197

10-4. Application of the Short-Circuit Impedance Matrix to Symmetrical Fault Studies 201

10-5. Application of the S-C Matrix to Line-to-Ground Faults 206

10-6. Application of the S-C Matrix to the Line-to-Line Fault 209

10-7. Application of the S-C Matrix to Double Line-to-Ground Faults 211

10-8. Method of Developing the S-C Impedance Matrix One Step at a Time..... 213

10-9.	Modifying the S-C Matrix to Accommodate System Changes	219
10-10.	A General Procedure for Developing the S-C Matrix	220
10-11.	Solving a Large Network by Sections	222
10-12.	Alternate Concept of the S-C Matrix Method	224
10-13.	Handling Mutuals Between Branch Lines	225
10-14.	Summary	226
	Problems	227

chapter 11

CONCEPTS IN THE CONTROL OF VOLTAGE, WATTS, AND VARS.....229

11-1.	Introduction	229
11-2.	Circulating Vars Between Transformers of Unequal Turns Ratios	230
11-3.	Circulating Vars Between Buses of Unequal Voltage Magnitudes	233
11-4.	Circulating Watts Between Buses of Differing Voltage Phase Angles	233
11-5.	A Two-Generator Experiment	235
11-6.	Controlling System Voltage with Appropriate Var Injections Using the Bus Impedance Matrix	239
11-7.	Paralleling Load-Tap-Changing (LTC) Transformers with Automatic Controls	246
11-8.	Overexcitation Protection for the Generator Transformer	249
11-9.	Modeling Transformers with Off-Nominal Turns Ratio	251
	Problems	254

chapter 12

SELECTED NUMERICAL METHODS AND THE LOAD-FLOW PROBLEM.....257

12-1.	Introduction	257
12-2.	Pivotal Condensation Applied to Determinants	258
12-3.	The Gauss Reduction Method	260
12-4.	The Crout Reduction Method	263
12-5.	Achieving a Matrix Solution and Inverse with Elementary Row Operations	266
12-6.	The Gauss Iteration Technique	269
12-7.	The Gauss-Seidel Iteration	271
12-8.	The Newton-Raphson Approach to Nonlinear Equations	271
12-9.	Defining the Load-Flow Problem	275
12-10.	Bus Admittance Concept for Load Studies with the Gauss-Seidel Iteration	278
12-11.	Bus Impedance Concept for Load Studies	286
12-12.	The Relaxation Method Applied to Eq. 12-60	290
12-13.	Load-Flow and the Newton-Raphson Method	291
12-14.	Summary	294
	Problems	295

chapter 13

ECONOMIC DISPATCH AND UNIT COMMITMENT297

13-1.	Introduction	297
13-2.	Operating Constraints.....	297
13-3.	Short-Term Load Forecasting	299
13-4.	Economic Load Distribution Between Generators Within a Station	301
13-5.	The Penalty-Factor Technique for Considering Line Losses	306
13-6.	Determination of the Loss Equation.....	307
13-7.	Solution Procedure for the Dispatch Problem	314
13-8.	Transforming an m -Load System into a System with One Composite Load Bus	324
13-9.	Unit Commitment (Scheduling) of Generators	327
13-10.	Methods for Scheduling Units	329
	Problems.....	330

chapter 14

SYSTEM STABILITY332

14-1.	Introduction	332
14-2.	The Electrical and Physical Significance of δ	333
14-3.	The Power-Transfer Equation	335
14-4.	Steady-State Stability Limit	336
14-5.	Short-Circuit Ratio (SCR) and Its Effect upon Stability	337
14-6.	Prevention of Pullout Under Steady-State Operating Conditions	340
14-7.	Some Factors in Determining the Voltage Control Point of Generators.....	344
14-8.	Transient Stability and the Swing Equation	345
14-9.	The Single-Machine Problem and the Equal-Area Criterion.....	349
14-10.	Transient Stability and the Point-by-Point Method	359
	Problems.....	365

appendix A

LINE PARAMETERS369

A-1.	Resistance Considerations	369
A-2.	Dielectric Field Relations for Parallel Plates, Point Charges, and Cylindrical Conductors.....	371
A-3.	Single-Phase Line Capacitance.....	373
A-4.	Capacitance of the Symmetrical Three-Phase Line.....	374
A-5.	Basic Inductance Relationships	377
A-6.	Partial Flux Linkages.....	379
A-7.	External Flux Linkages.....	380
A-8.	Total Inductance and Conductor GMR	380
A-9.	Single-Phase Line Inductance	381
A-10.	Three-Phase Line Inductance	382

appendix B	
CIRCLE DIAGRAMS	385
B-1. Introduction	385
B-2. Sending and Receiving Circles	386
appendix C	
FUNDAMENTALS OF DETERMINANTS AND MATRICES	393
C-1. Determinants	393
C-2. Basic Properties of Matrices	397
INDEX	403