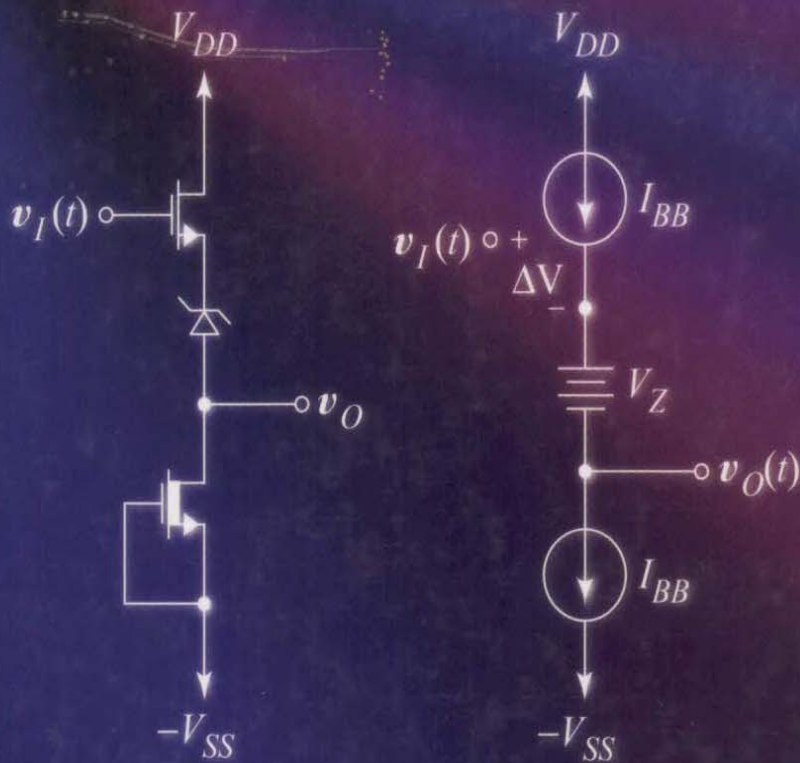


# ELECTRONIC CIRCUITS

Analysis, Simulation, and Design



NORBERT R. MALIK

# Contents

<b>CHAPTER 1</b>			
<b>Modeling and Signal Processing Principles</b>			
1	1.1	Human/Computer Synergism	3
	1.2	Volt-Ampere and Transfer Characteristics	4
	1.3	Ideal Amplifiers	12
	1.4	Input, Output, and Interstage Loading	17
	1.5	Difference Amplifiers	23
	1.6	Other Amplifier Limitations	41
	1.7	Summary	54
		References	55
		Problems	56
<b>CHAPTER 2</b>			
<b>Operational Amplifier Circuits</b>			
65	2.1	The Operational Amplifier	65
	2.2	Memoryless Op Amp Circuits	67
	2.3	Op Amp Circuits with Memory	79
	2.4	Simulations of Infinite-Gain Op Amp Circuits	87
	2.5	Second-Order Effects in Operational Amplifiers	91
	2.6	Circuits Without Negative Feedback	118
	2.7	Summary	122
		References	123
		Problems	123
<b>CHAPTER 3</b>			
<b>Semiconductors, pn Junctions, and Diode Circuits</b>			
134	3.1	Conduction in Insulators and Metals	135
	3.2	Conduction in Intrinsic Semiconductors	139
	3.3	Doped Semiconductors	144
	3.4	Diffusion of Holes and Electrons	151
	3.5	The pn Junction in Equilibrium	154
	3.6	The Junction Diode	157
	3.7	Large-Signal Diode Models	162
	3.8	Static SPICE Model for the Diode	175
	<b>ix</b>		

	9.6	Stability of Feedback Amplifiers	704
	9.7	Sinusoidal Oscillators	721
	9.8	Summary	739
		Reference	741
		Problems	742
<b>CHAPTER</b>	10.1	Transistor Power Dissipation	752
<b>10</b>	10.2	Power Transistors	756
<b>Power Circuits</b>	10.3	Class A Amplifier	759
<b>and Systems</b>	10.4	Nonlinear Distortion	766
751	10.5	Class B Amplifier	770
	10.6	Class AB Amplifier	777
	10.7	Power Op Amps	791
	10.8	Class D Amplifier	794
	10.9	Power Supplies	800
	10.10	Summary	811
		References	812
		Problems	812
<b>CHAPTER</b>	11.1	Operational Transconductance Amplifier	820
<b>11</b>	11.2	The 741 Operational Amplifier: A Case Study	828
<b>Analog Integrated</b>	11.3	Analog Multiplier	843
<b>Circuits</b>	11.4	Phase-Locked Loop	861
820	11.5	Summary	883
		References	884
		Problems	884
<b>CHAPTER</b>	12.1	Basic Filter Concepts	890
<b>12</b>	12.2	Second-Order Active Filters	898
<b>Filters</b>	12.3	Lowpass Active Filters of Order $n$	918
890	12.4	Active Extensions of Passive Filter Theory	934
	12.5	Switched-Capacitor Circuits	942
	12.6	Summary	951
		References	952
		Problems	953
<b>CHAPTER</b>	13.1	Ideal Logic Gates and Practical Approximations	959
<b>13</b>	13.2	NMOS and PMOS Logic Circuits	963
<b>Digital Logic</b>	13.3	CMOS Logic Circuits	974
<b>Circuits</b>	13.4	Gallium Arsenide Logic Circuits	984
958	13.5	TTL Logic Circuits	991

	13.6	Emitter-Coupled Logic	1010
	13.7	Summary	1019
		References	1021
		Problems	1021
<b>CHAPTER</b>	14.1	Bistable Circuits	1028
<b>14</b>	14.2	Monostable Circuits	1042
<b>Digital Memory,</b>	14.3	Astable Circuits	1045
<b>Interfacing, and</b>	14.4	555 Timer Circuit	1050
<b>Timing Principles</b>	14.5	Interfacing Problems and Principles	1054
1028	14.6	Transmission Line Effects	1083
	14.7	Solid-State Memory	1112
	14.8	Summary	1124
		References	1126
		Problems	1126
		<b>Appendix A</b> Spice Analysis	1139
		<b>Appendix B</b> Two-Port Networks	1143
		<b>Appendix C</b> SPICE Parameters for Bipolar Transistors	1158