

MICROPROCESSORS AND MICROSYSTEMS

Volume 24, Number 5, 1 September 2000

Abstracted/indexed in: ACM Computing Reviews, Biomedical Engineering Citation Index, Cambridge Sci. Abstr. and Computer Abstracts, Compumath Citation Index, Current Contents, Current Technology Index, Electronics and Communications Abstr., Engineering Index, INSPEC, Research Alert, Science Citation Index and Scisearch

Contents

M.W. El-Kharashi, F. ElGuibaly, K.F. Li

A quantitative study for Java microprocessor architectural requirements. Part I: Instruction set design 225

M.W. El-Kharashi, F. ElGuibaly, K.F. Li

A quantitative study for Java microprocessor architectural requirements. Part II: high-level language support 237

M.H. Wong, T.S. Chung, Y.K. Wong

An evolving neural network approach in unit commitment solution 251

F.J. Mora, Á. Sebastiá, H. Müller

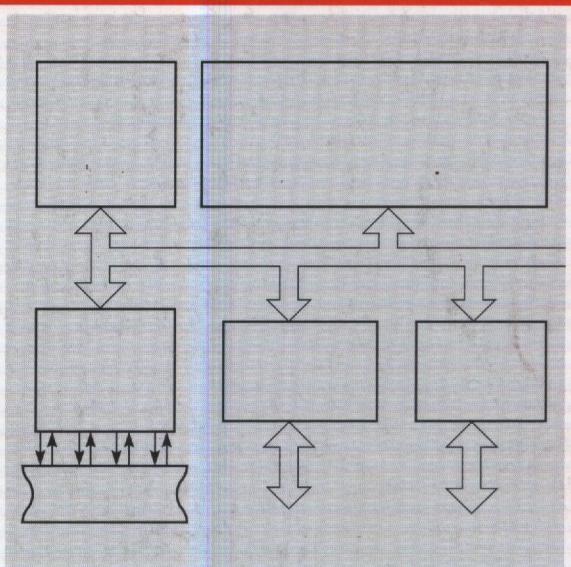
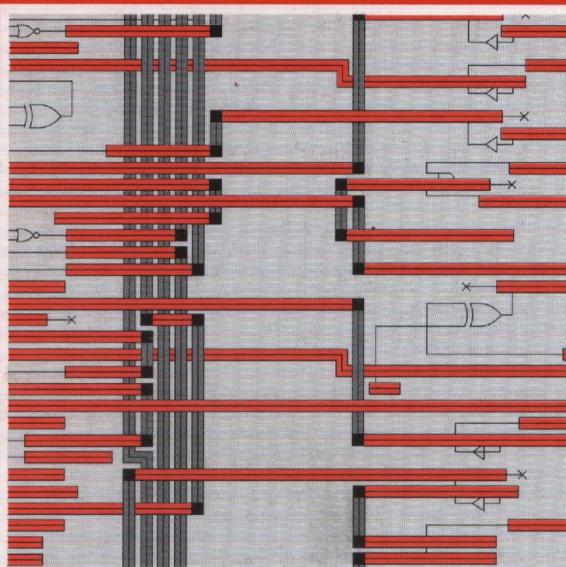
VME crate interconnection through the SCI network in large data acquisition systems..... 263

J.N. Lygouras

Accurate velocity evaluation using adaptive sampling interval 269

Keep track of recently published papers via the journal homepage at
[WWW:http://www.elsevier.nl/locate/micpro](http://www.elsevier.nl/locate/micpro)

MICROPROCESSORS AND MICROSYSTEMS



In this Issue

- Architectural requirements for Java microprocessors: Instruction set design and high-level language support
- Unit commitment solution: an evolving neural network approach
- VME crate interconnection through the SCI network
- Accurate velocity evaluation using

