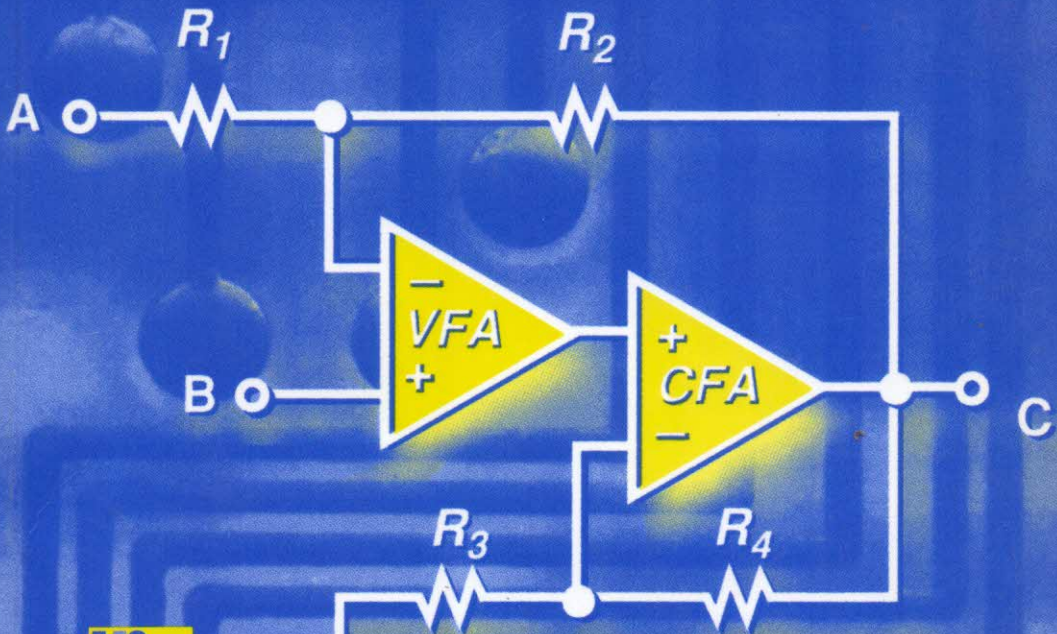


DESIGN WITH OPERATIONAL AMPLIFIERS AND ANALOG INTEGRATED CIRCUITS

SECOND EDITION



McGRAW-HILL INTERNATIONAL EDITIONS

Electrical Engineering Series

SERGIO FRANCO

CONTENTS

Preface	xiii
1 Operational Amplifier Fundamentals	1
1.1 Amplifier Fundamentals	3
1.2 The Operational Amplifier	5
1.3 Basic Op Amp Configurations	8
1.4 Ideal Op Amp Circuit Analysis	15
1.5 Negative Feedback	23
1.6 Feedback in Op Amp Circuits	30
1.7 The Loop Gain	36
1.8 Op Amp Powering	41
Problems	46
Bibliography	56
Appendix 1A: Standard Resistor Values	57
2 Circuits with Resistive Feedback	58
2.1 Current-to-Voltage Converters	59
2.2 Voltage-to-Current Converters	62
2.3 Current Amplifiers	69
2.4 Difference Amplifiers	71
2.5 Instrumentation Amplifiers	77
2.6 Instrumentation Applications	84
2.7 Transducer Bridge Amplifiers	90
Problems	97
References	104
3 Active Filters: Part I	105
3.1 The Transfer Function	108
3.2 First-Order Active Filters	114
3.3 Audio Filter Applications	121
3.4 Standard Second-Order Responses	126
3.5 <i>KRC</i> Filters	133
3.6 Multiple-Feedback Filters	141
3.7 State-Variable and Biquad Filters	144

3.8	Sensitivity	151
	Problems	153
	References	159
4	Active Filters: Part II	160
4.1	Filter Approximations	161
4.2	Cascade Design	167
4.3	Generalized Impedance Converters	176
4.4	Direct Design	182
4.5	The Switched Capacitor	186
4.6	Switched-Capacitor Filters	193
4.7	Universal SC Filters	200
	Problems	205
	References	211
5	Static Op Amp Limitations	213
5.1	Simplified Op Amp Circuit Diagram	214
5.2	Input Bias and Offset Currents	220
5.3	Low-Input-Bias-Current Op Amps	223
5.4	Input Offset Voltage	228
5.5	Low-Input-Offset-Voltage Op Amps	233
5.6	Input Offset-Error Compensation	238
5.7	Maximum Ratings	243
	Problems	247
	References	251
	Appendix 5A: Datasheets of the μ A741 Op Amp	252
6	Dynamic Op Amp Limitations	261
6.1	Open-Loop Response	262
6.2	Closed-Loop Response	267
6.3	Input and Output Impedances	272
6.4	Transient Response	278
6.5	Effect of Finite GBP on Integrator Circuits	286
6.6	Effect of Finite GBP on Filters	293
6.7	Current-Feedback Amplifiers	297
	Problems	306
	References	312

7	Noise	313
7.1	Noise Properties	315
7.2	Noise Dynamics	319
7.3	Sources of Noise	324
7.4	Op Amp Noise	330
7.5	Noise in Photodiode Amplifiers	338
7.6	Low-Noise Op Amps	342
	Problems	345
	References	348
8	Stability	350
8.1	The Stability Problem	351
8.2	Stability in Constant-GBP Op Amp Circuits	358
8.3	Internal Frequency Compensation	368
8.4	External Frequency Compensation	377
8.5	Stability in CFA Circuits	383
8.6	Composite Amplifiers	387
	Problems	392
	References	399
9	Nonlinear Circuits	400
9.1	Voltage Comparators	401
9.2	Comparator Applications	410
9.3	Schmitt Triggers	418
9.4	Precision Rectifiers	424
9.5	Analog Switches	431
9.6	Peak Detectors	436
9.7	Sample-and-Hold Amplifiers	439
	Problems	445
	References	451
10	Signal Generators	452
10.1	Sine Wave Generators	454
10.2	Multivibrators	460
10.3	Monolithic Timers	469
10.4	Triangular Wave Generators	475
10.5	Sawtooth Wave Generators	479

10.6	Monolithic Waveform Generators	482
10.7	<i>V-F</i> and <i>F-V</i> Converters	490
	Problems	497
	References	502
11	Voltage References and Regulators	503
11.1	Performance Specifications	504
11.2	Voltage References	510
11.3	Voltage-Reference Applications	517
11.4	Linear Regulators	523
11.5	Linear-Regulator Applications	531
11.6	Switching Regulators	540
11.7	Monolithic Switching Regulators	549
	Problems	557
	References	563
12	D-A and A-D Converters	564
12.1	Performance Specifications	566
12.2	D-A Conversion Techniques	572
12.3	Multiplying DAC Applications	586
12.4	A-D Conversion Techniques	590
12.5	Oversampling Converters	601
	Problems	609
	References	611
13	Nonlinear Amplifiers and Phase-Locked Loops	612
13.1	Log/Antilog Amplifiers	612
13.2	Analog Multipliers	620
13.3	Operational Transconductance Amplifiers	625
13.4	Phase-Locked Loops	633
13.5	Monolithic PLLs	641
	Problems	649
	References	652
	Index	655